

100

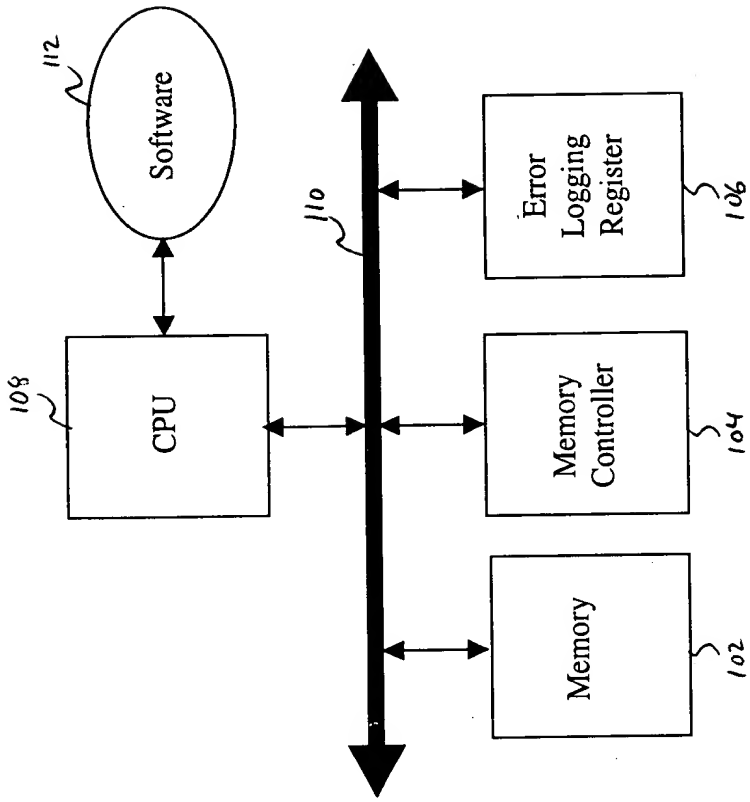


Fig. 1

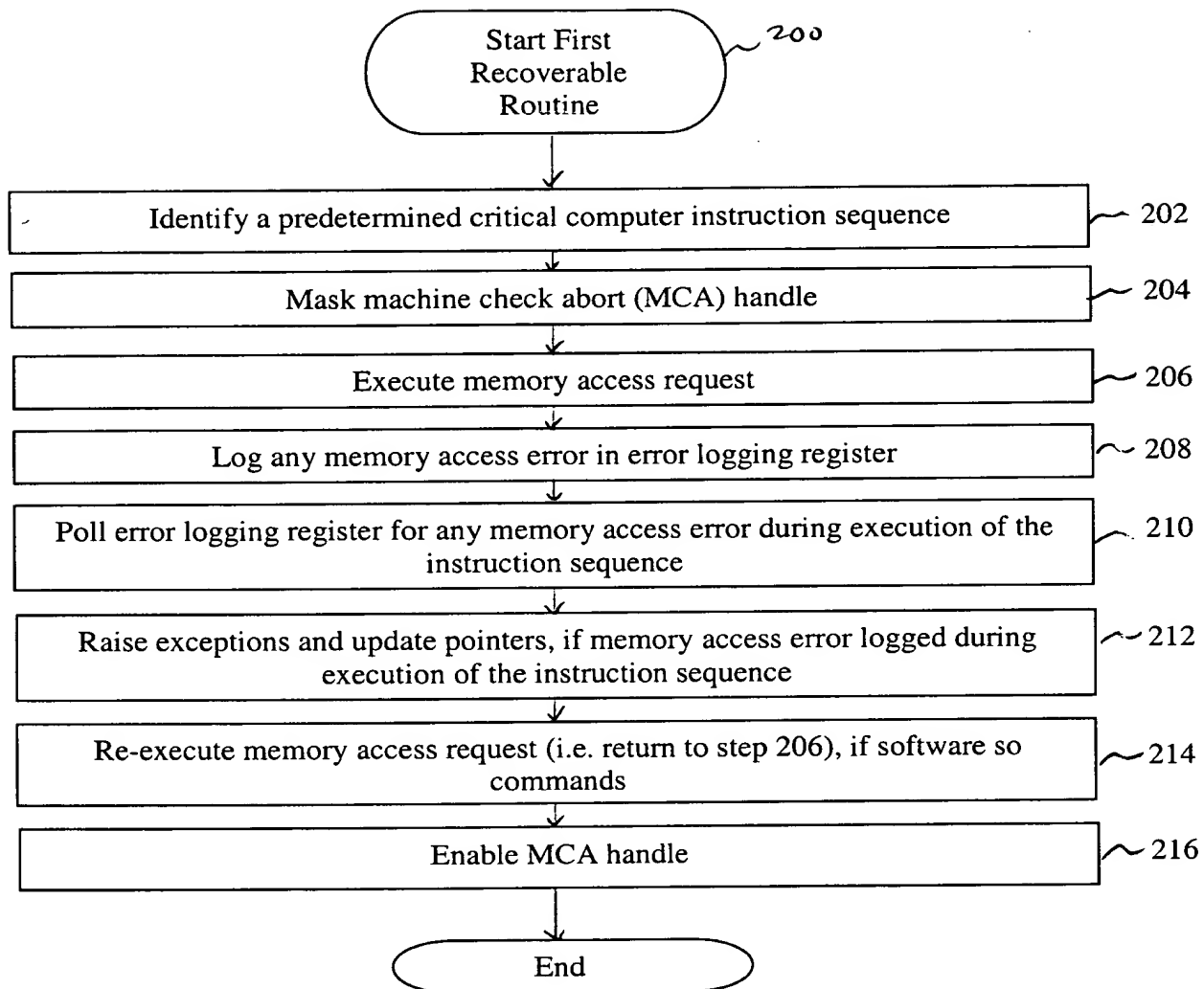


Fig. 2

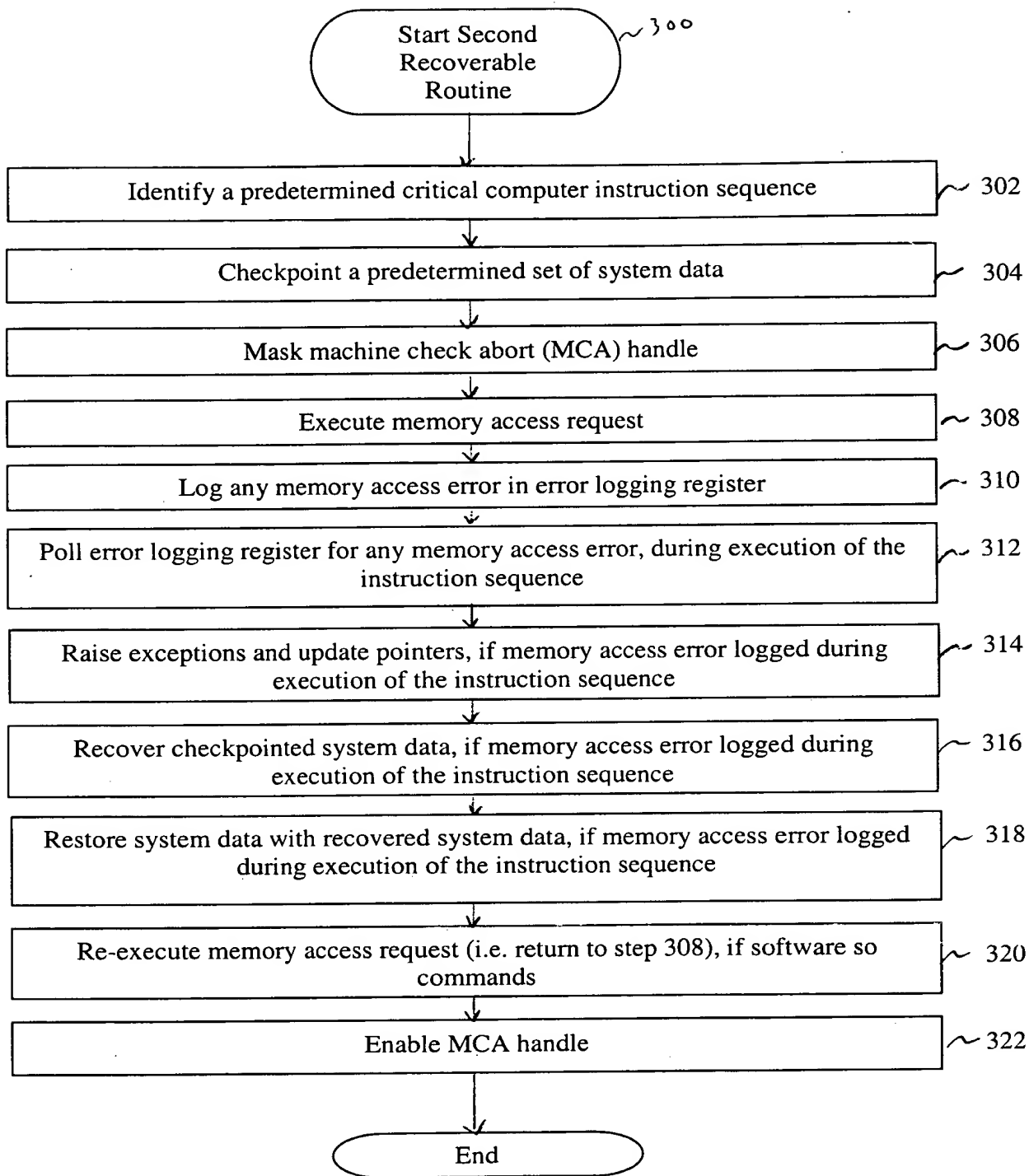


Fig. 3

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graph TD; 400([Start Third Recoverable Routine]) --> 402[Identify a predetermined critical computer instruction sequence]; 402 --> 404[Mask machine check abort (MCA) handle]; 404 --> 406[Execute memory access request]; 406 --> 408[Log any memory access error in error logging register]; 408 --> 410[Set data returned in response to the memory access request equal to predefined fake data, if memory access error logged during execution of the instruction sequence]; 410 --> 412[Receive data returned in response to memory access request]; 412 --> 414[Skip to step 422, if data returned is not equivalent to the predefined fake data]; 414 --> 416[Poll error logging register for any memory access error during execution of the instruction sequence]; 416 --> 418[Raise exceptions and update pointers, if memory access error logged during execution of the instruction sequence]; 418 --> 420[Re-execute memory access request (i.e. return to step 406), if software so commands]; 420 --> 422[Enable MCA handle]; 422 --> End([End]);
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Fig. 4